



#11/C
9-23-2
Roberto
amdt

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/810,595
Filing Date March 15, 2001
Inventor Belford T. Coursey
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Yennhu B. Huynh
Attorney's Docket No. MI22-1660
Title: Memory Circuitry With Array Area And Peripheral Area (As amended)

EV182663205US

RESPONSE TO JULY 1, 2002 OFFICE ACTION

To: Assistant Commissioner for Patents
Attn: Art Unit 2813
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Office Action dated July 1, 2002, Applicant amends
and remarks as follows:

AMENDMENTS

In the Title

Delete the existing Title and substitute the following in its place:

Memory Circuitry With Array Area And Peripheral Area

TECHNOLOGY CENTER 2800

SEP 17 2002

RECEIVED